

13 ~~from the stack pointer register to the second stack save area if the least significant bit of the stack~~  
14 ~~pointer register indicates the second word size such that the width indication bit in the first stack~~  
15 ~~save area in memory indicates that the data values for the procedure have the second word size].~~

1 ~~34. The method of claim 33, wherein the first stack save area is specified by a stack pointer value~~  
2 ~~in the stack pointer register.~~

1 ~~35. The method of claim 34, wherein the second stack save area is specified by the stack pointer~~  
2 ~~value in the stack pointer register plus an offset value that corresponds to an area in memory~~  
3 ~~required for the first stack save area.~~

1 ~~36. The method of claim 33, wherein the first word size comprises 32 bits and the second word~~  
2 ~~size comprises 64 bits.~~

1 ~~37. The method of claim 33, wherein the registers in the processor and the stack pointer register in~~  
2 ~~the processor comprise 16 registers each comprising 64 bits.~~

1 ~~38. The method of claim 35, wherein the offset value and the area in memory for the first stack~~  
2 ~~save area each comprise 16 multiplied by 4 bytes per register.~~

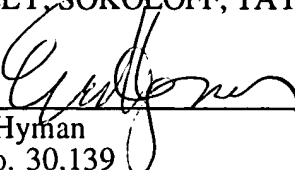
#### REMARKS

Entry of the foregoing amendments prior to the initial examination of the above-captioned application is requested.

Respectfully submitted,

BLAKELY, SOKOLOFF, TAYLOR & ZAFMAN

Dated: October 21, 1997

  
Eric S. Hyman  
Reg. No. 30,139

12400 Wilshire Boulevard  
Seventh Floor  
Los Angeles, California 90025  
(310) 207-380